Place: Lovett Commons

Time: 6:30 - 7:30

Members: Undergraduate members

* ECOG data: TK took the training module and is now requesting ECOG data as fast as possible
* AFE:
  + you can use normal CMOS mode if you pull a pin high, but we don’t know if we can do that, so we may need a converter
  + 64 channel uses double data rate by transmitting on falling and rising clock edge, which requires a FPGA to run
    - We can get only the 32 channel
* **HIGH PRIORITY SPECS due on FRIDAY**
* logistics:
* TCP is more heavyweight: handles reliability and congestion control
* Compression:
  + Yuan has code, but can’t test until we get actual ECOG data
* Transmission
  + Xin: look into how to implement TCP and/or UDP or what other transmission format to use (or look into the common bluetooth transmission scheme (RCOM))
    - Try to get an example running on a microcontroller
    - What will be overhead size?
    - error rate and error checking mechanism?
    - What does it do if it loses a package?
  + Yuan: Generate data to test huffman encoding and come up with some sort of compression ratio and compare it with the correlation function of the signal
    - Minimum:
      * What affects compression ratio of a given algorithm?
  + TK: Nordic board and inquire about AFE whether or not we can pull the pin up to use CMOS mode, not DDR
  + Stephen: CC2650
* AGENDA:
  + Check with Tandon: What is the minimum bandwidth we can get away with?
    - Present power analysis and bandwidth analysis